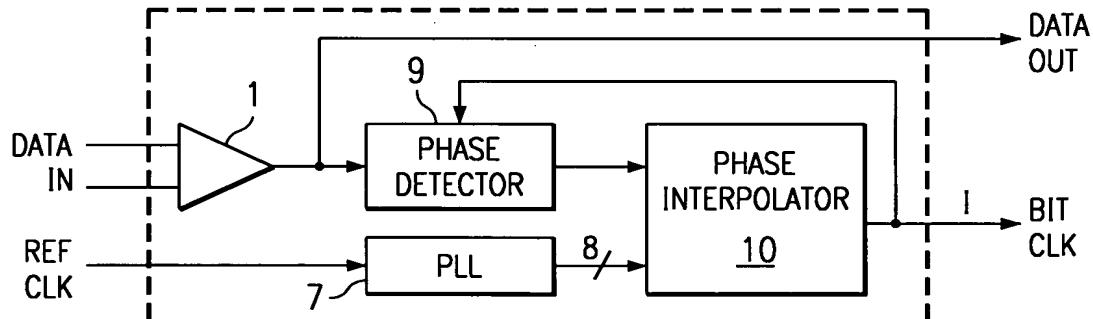
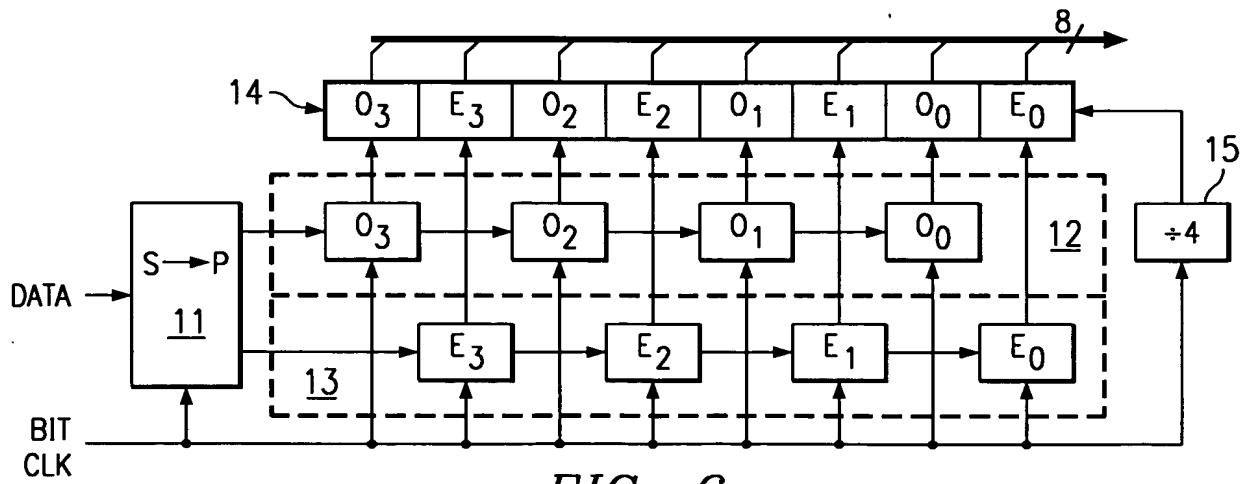
*FIG. 3**FIG. 4**FIG. 5**FIG. 6*



TI-32313

2/9

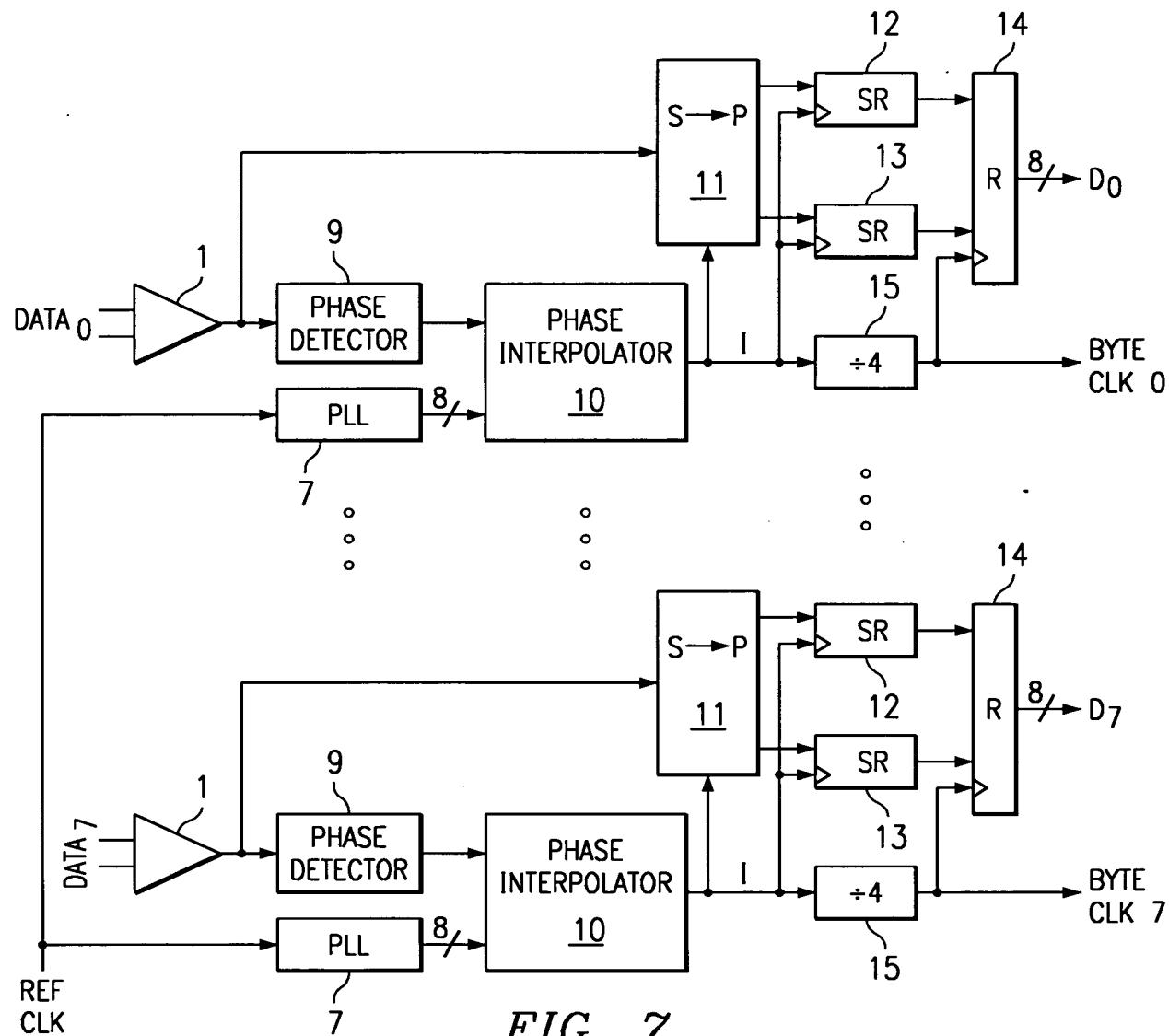


FIG. 7

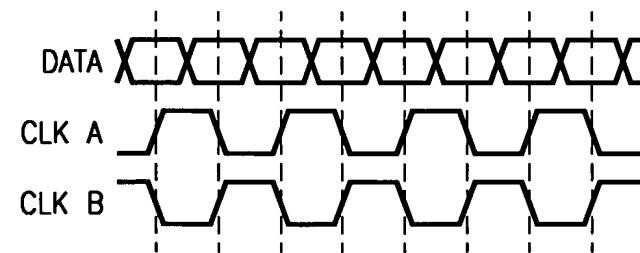


FIG. 8



FIG. 9

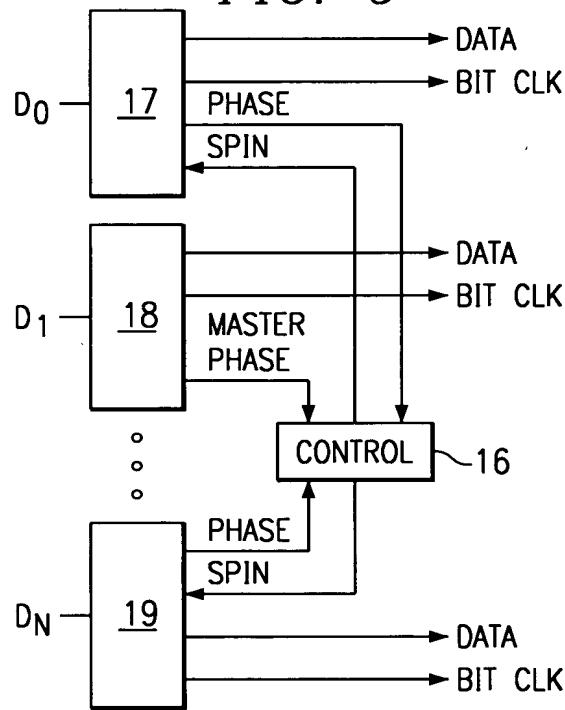


FIG. 10

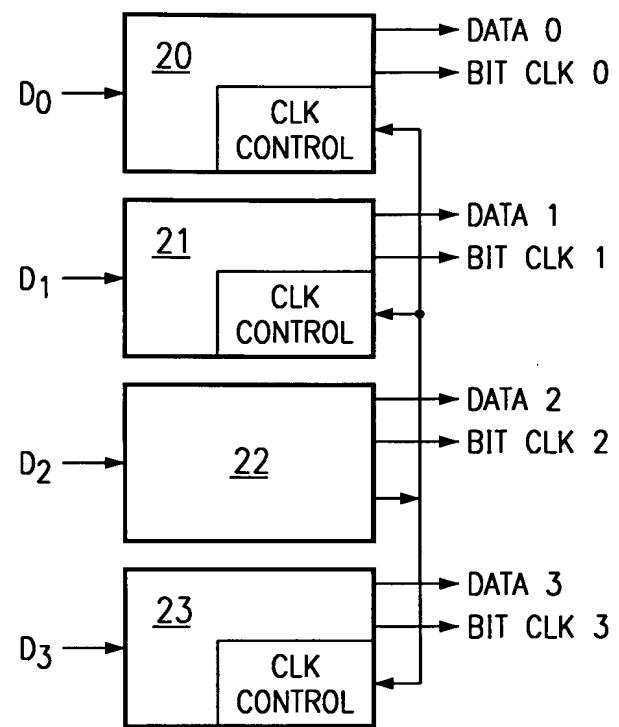
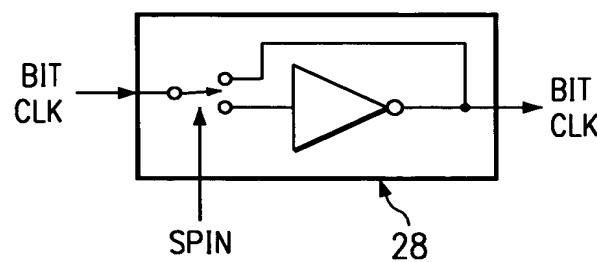


FIG. 9a



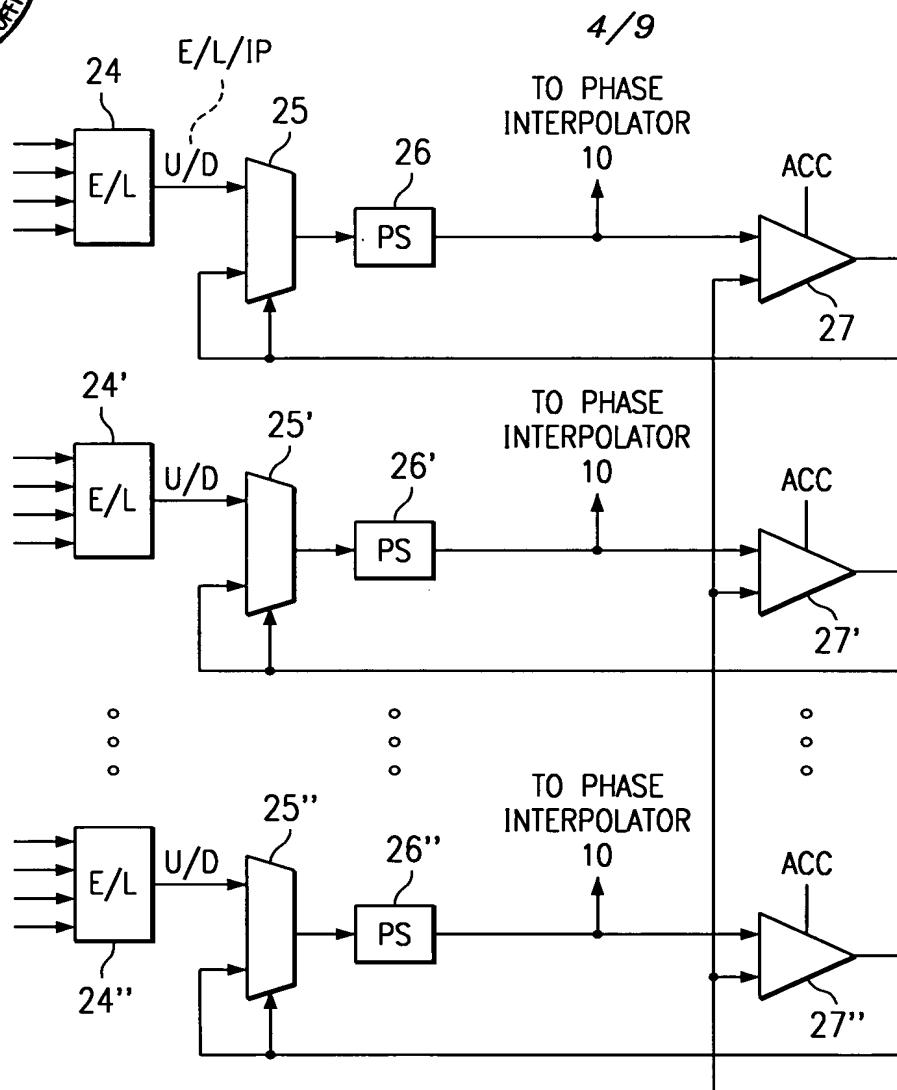


FIG. 11

MASTER BIT CLOCK PHASE FROM 22

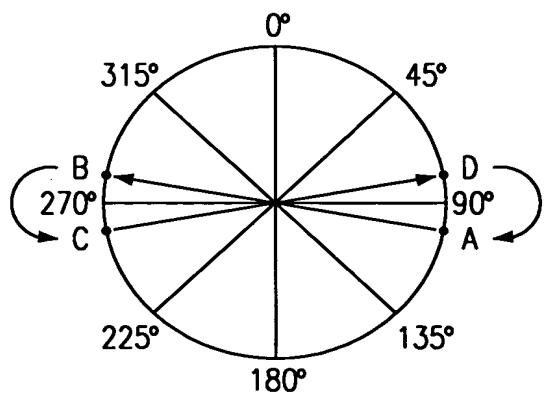


FIG. 12

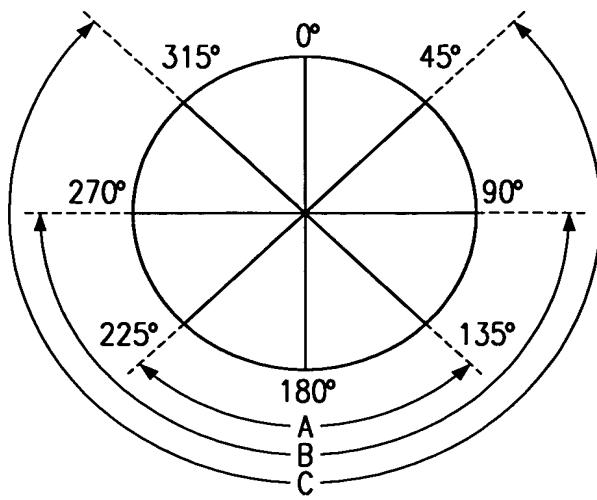
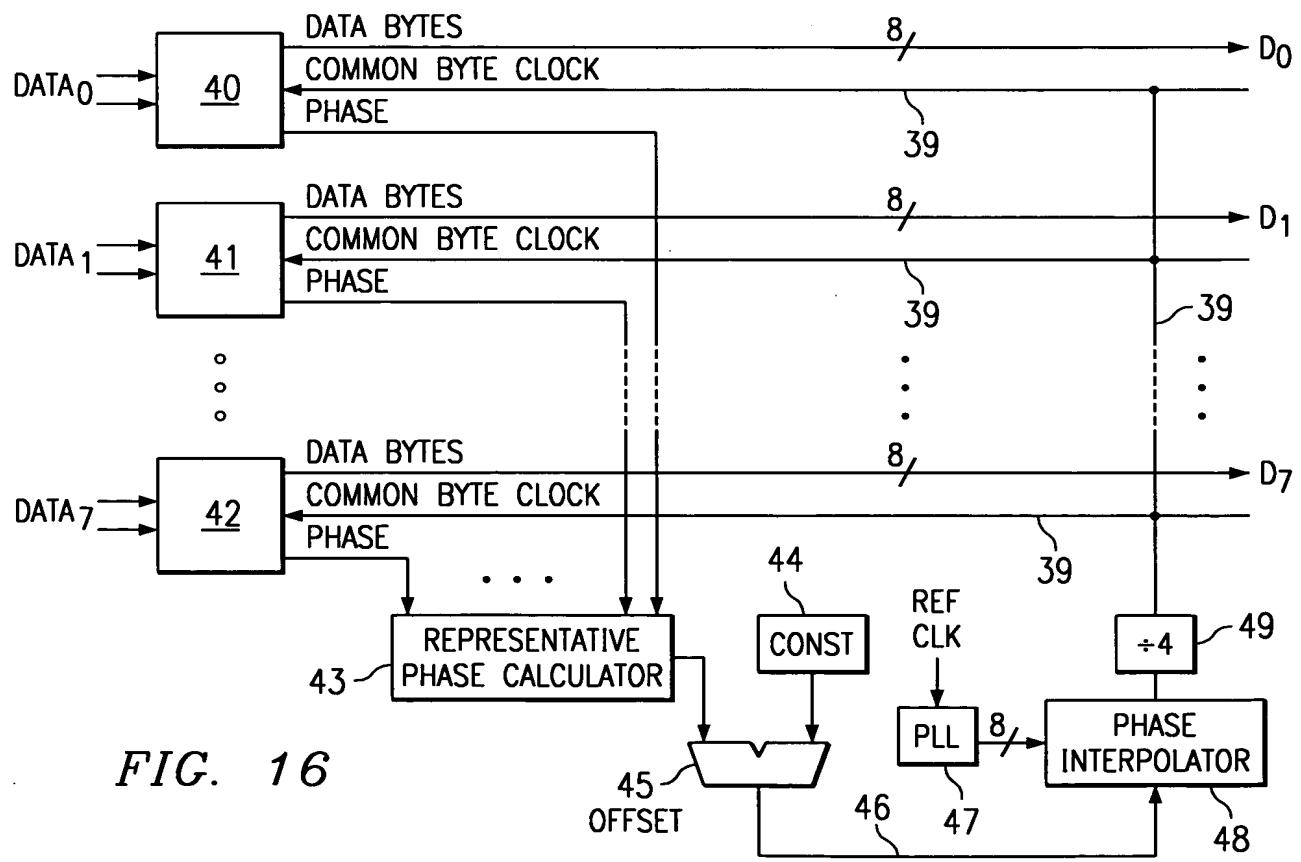
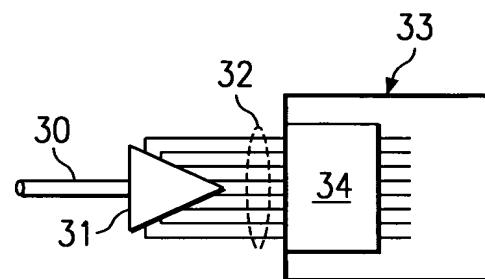
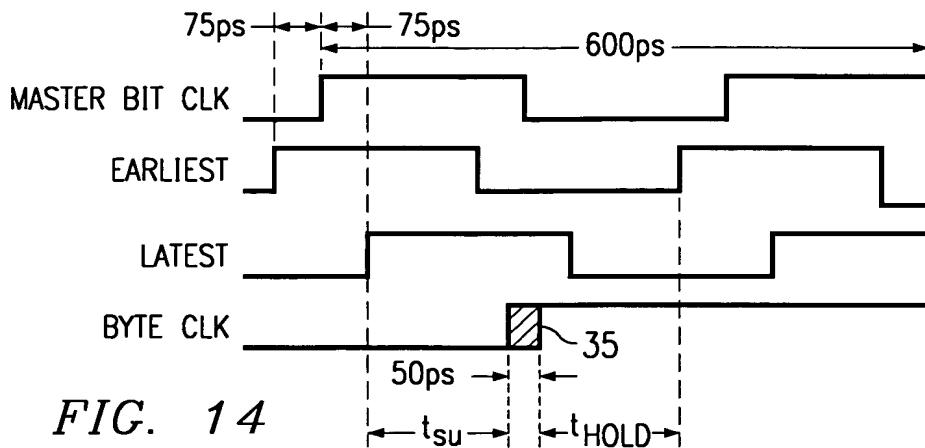


FIG. 13



5/9





6/9

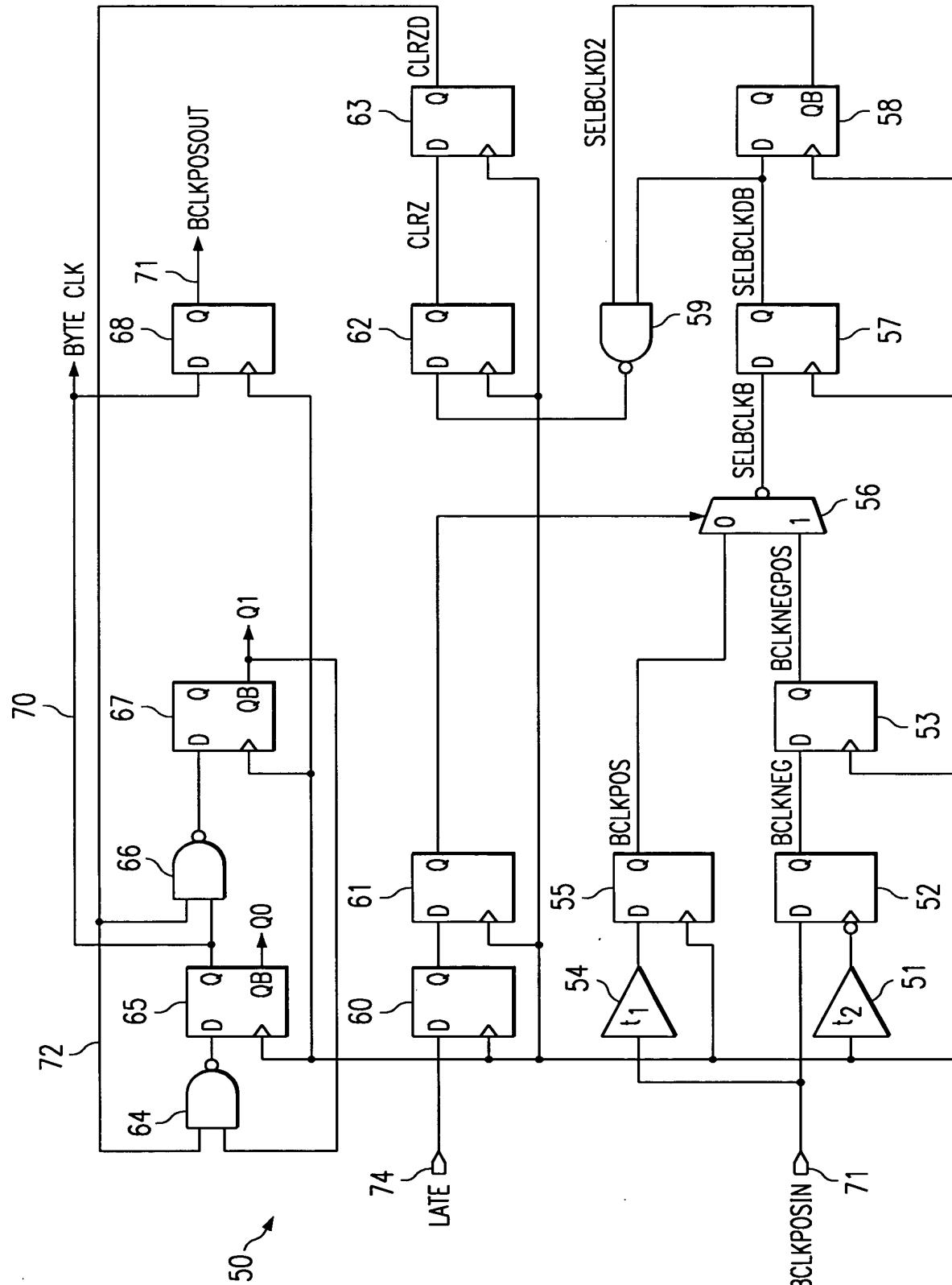


FIG. 17



7/9

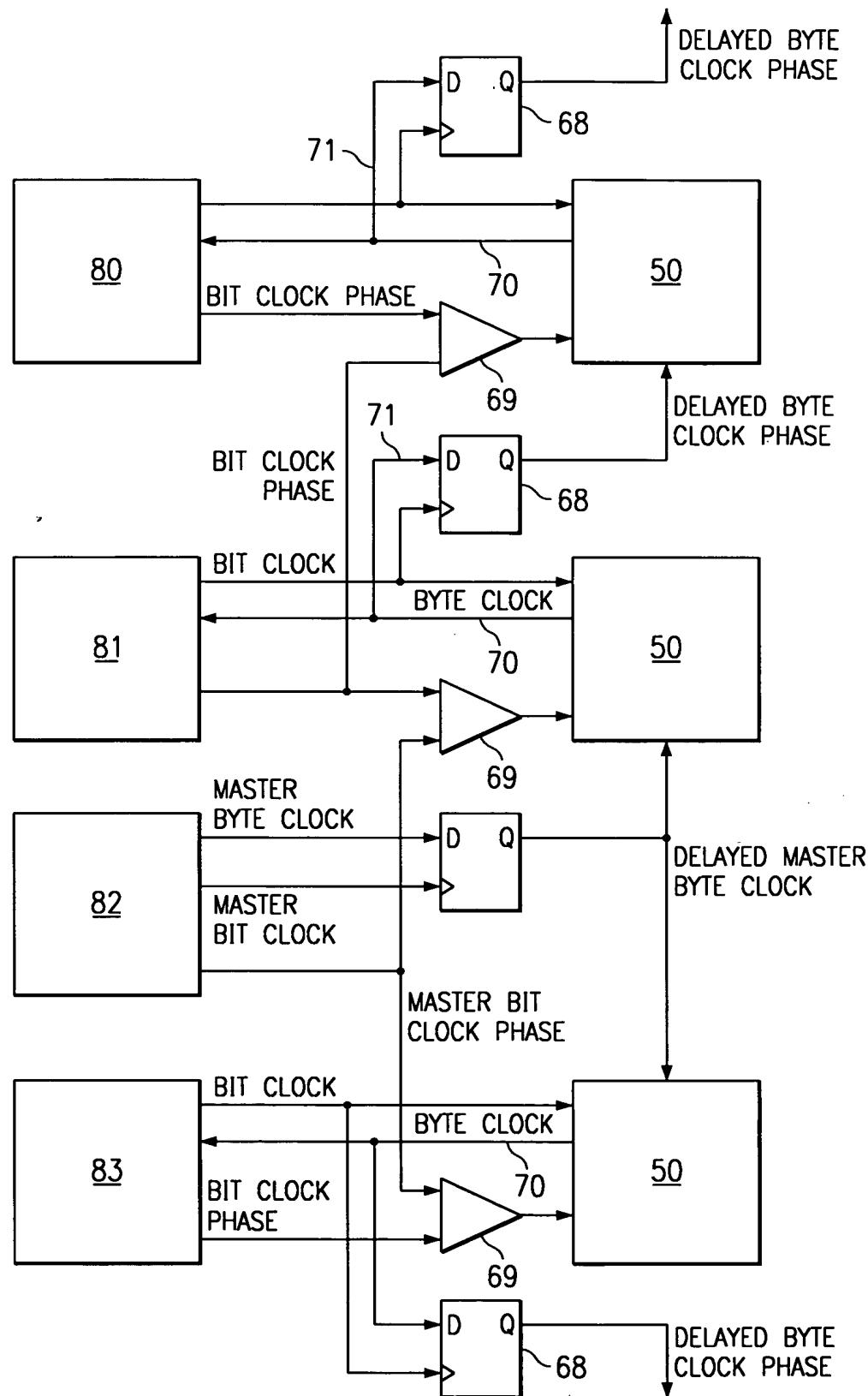
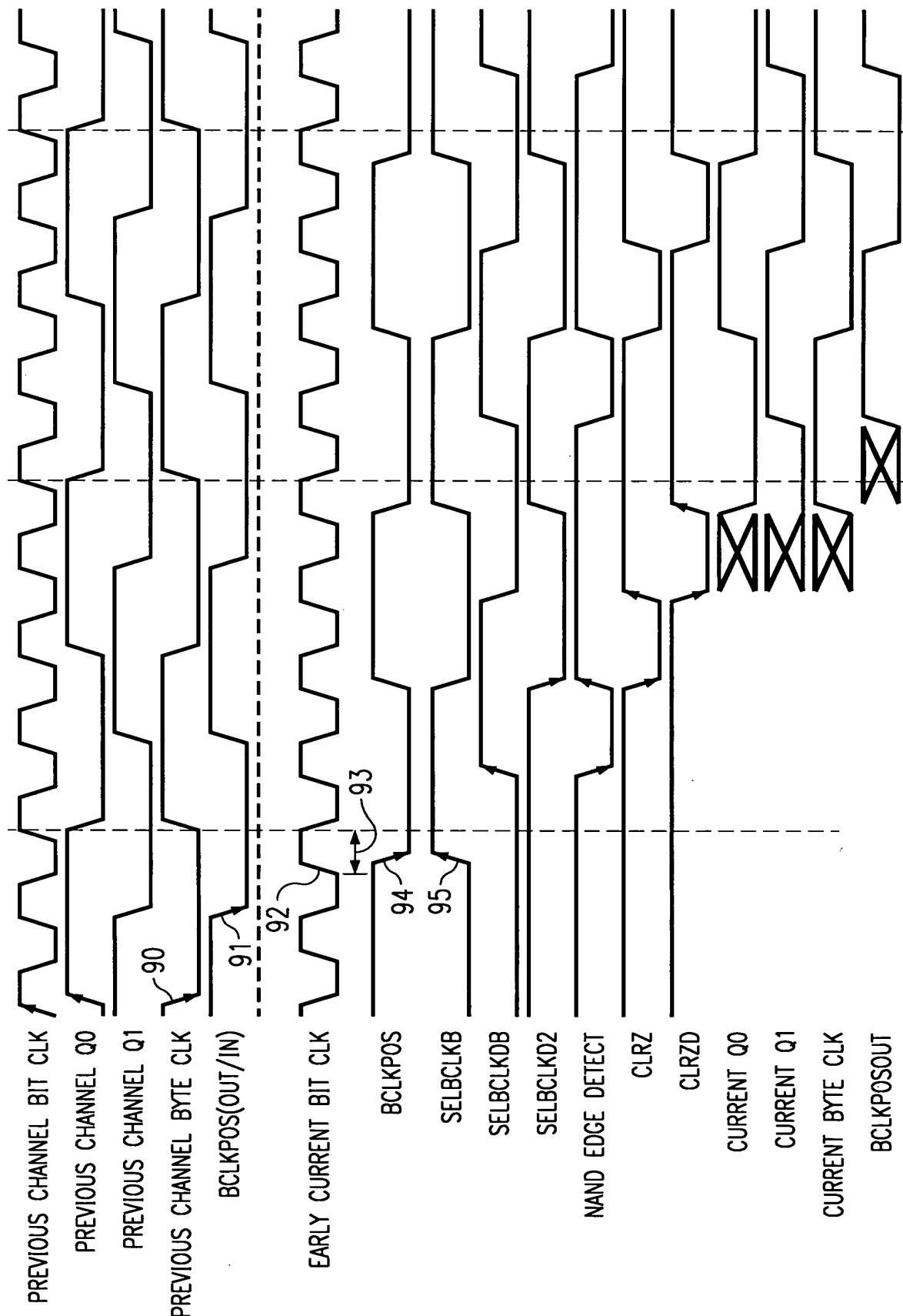


FIG. 18



8/9

FIG. 19





9/9

FIG. 20

